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	Application No.	Applicant(s)
	10/502,354	BOUZIDI, JEAN-PIERRE
Notice of Allowability	Examiner	Art Unit
<i>x</i> 's	Daniel Swerdlow	2646
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>application filed 23 July 2004</u> .		
2. The allowed claim(s) is/are <u>1-3.</u>		
<ul> <li>3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a)  All b)  Some* c) None of the:</li> <li>1.  Certified copies of the priority documents have been received.</li> <li>2.  Certified copies of the priority documents have been received in Application No</li> </ul>		
<ol> <li>Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ol>		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. X CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1) ☐ hereto or 2) ☐ to Paper No./Mail Date		
(b) ⊠ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s)	E   Notice of Information	atent Application (PTO-152)
1. Notice of References Cited (PTO-892)	<u> </u>	,, , ,
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary Paper No./Mail Dat	
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0		
Paper No./Mail Date  4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛛 Examiner's Stateme	ent of Reasons for Allowance
5. 2.5.5 <b>3.5</b> 00	9.  Other	,
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## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. David J. Cushing, reg. no. 28,703 on 16 December 2005.

The application has been amended as follows:

- 2. The following changes to the drawings have been approved by the examiner and agreed upon by applicant:
- 3. Label Fig. 1 as --Prior Art--.
- 4. In order to avoid abandonment of the application, applicant must make these above agreed upon drawing changes.
- 5. The following is an examiner's statement of reasons for allowance:
- 6. Regarding Claim 1, US Patent 4,612,417 to Toumani discloses a battery feed circuit (Fig. 1) with an input stage (10, 11, 32, 33, 51, 52, 49, 17, 18, 36) comprising

two channels each connecting one input (10, 11) to one output (non-inverting and inverting inputs of 36); each channel comprising first means for reducing interference, these first means comprising, on each of the channels, means (input stages of 17, 18) for adding to the input voltage of the channel concerned a first feedback voltage (Vcm), and means (51, 52, 49) for supplying a first feedback voltage that is equal to half the sum of the voltages present at the inputs (10, 11), respectively, with the opposite sign. However,

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Toumani does not disclose means for further adding to the input voltage of each channel a second feedback voltage and means for supplying a second feedback voltage that is a function of the voltage at the input corresponding to this channel, with the opposite sign, and with a delay identical to that caused by the components of the means for supplying the first feedback voltage, as claimed. US Patent 3,516,005 to Brown, US Patent 3,761,831 to Foerster and US Patent 5,329,585 to Susak et al. also disclose addition of a first feedback voltage that is equal to half the sum of the voltages present at the inputs (10, 11), respectively, with the opposite sign in order to reduce common mode interference. However, none these discloses means for further adding to the input voltage of each channel a second feedback voltage and means for supplying a second feedback voltage that is a function of the voltage at the input corresponding to this channel, with the opposite sign, and with a delay identical to that caused by the components of the means for supplying the first feedback voltage, as claimed. As such, there is no teaching or suggestion in the prior art or the knowledge of one skilled in the art to modify the circuit disclosed in Toumani to produce the claimed invention. As such, Claim 1 is allowable.

7. Claims 2 and 3 are allowable due to dependence from Claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel Swerdlow whose telephone number is 571-272-7531. The examiner can normally be reached on Monday through Friday between 7:30 AM and 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sinh H. Tran can be reached on 571-272-7564. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Daniel Swerdlow

Examiner

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16 December 2005